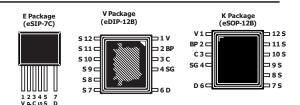
Application Note AN-112 Failure Mode and Effect Analysis TinySwitch5 Family



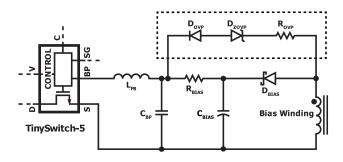
Failure Mode Analysis Summary

Device Level Failure Mode Analysis

Table 1 is the device level failure mode analysis including the system effects of an open-circuit for each pin as well as adjacent pin-to-pin shorts. Recommended additional circuitry needed for a safe failure (C and BP open). In other cases, a safe failure is expected.



Pin Open	Prior to Power-Up	After Power-Up
D	No switching	IC stops switching
All S pins and SG	No Switching	IC stops switching
С	IC damage if no external OVP circuit (Figure 1)	IC damage if no external OVP circuit (Figure 1)
ВР	IC damage if no external Zener on BP capacitor (Figure 2)	IC damage if no external Zener on BP capacitor (Figure 2)
V	No Switching	No Switching
Pin-to-Pin Short	Prior to Power-Up	After Power-Up
D & S / SG	No switching. Fuse will be open.	IC stops switching. Fuse will be open.
C & S / SG	Switching stop then resume repeatedly. Vo out of regulation.	Switching stop then resume repeatedly. Vo out of regulation.
C & BP	IC stops switching	IC stops switching
BP & V	No Switching	Stopped switching



External BP Zener

C_{BP}

V_{ZenerBP}

V_{ZenerBP}

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Figure 1 - External OVP Circuit

Figure 2 - External BP Zener

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Revision	Notes	Date
Α	Initial Release.	01/25

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