

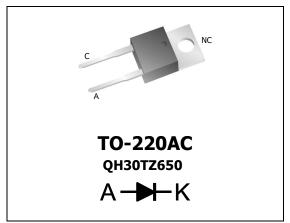
QH30TZ650 Qspeed[™] Family

650 V, 30 A H-Series PFC Diode

Product Summary

${ m I}_{\sf F(AVG)}$	30	Α
V_{RRM}	650	V
Q _{RR} (Typ at 125 °C)	180	nC
I _{RRM} (Typ at 125 °C)	5.8	Α
Softness t _B /t _A (Typ at 125 °C)	0.3	

Pin Assignment



RoHS Compliant

Package uses Lead-free plating and Green mold compound. Halogen free per IEC 61249-2-21.

General Description

This device has the lowest Q_{RR} of any 650 V silicon diode. Its recovery characteristics increases efficiency, reduces EMI and eliminates snubbers. Replaces SiC diodes for similar efficiency performance in high switching frequency applications.

Applications

- Power Factor Correction boost diode
- Server power supplies
- Motor drive circuits
- DC-AC inverters
- · Output rectifier

Features

- •
- High dI_F/dt capable (1000 A / μs)
- Soft recovery

Benefits

- Increases efficiency
 - Eliminates need for snubber circuits
 - Reduces EMI filter component size & count
- Enables extremely fast switching

Absolute Maximum Ratings

Absolute maximum ratings are the values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Symbol	Parameter	Conditions	Rating	Units
V_{RRM}	Peak repetitive reverse voltage	T _J = 25 °C	650	V
I _{F(AVG)}	Average forward current	T _J = 150 °C, T _C = 25 °C	30	Α
I_{FSM}	Non-repetitive peak surge current	60 Hz, ½ cycle, T _C = 25 °C	160	Α
I_{FSM}	Non-repetitive peak surge current	$1/2$ cycle of t = 28 μ s Sinusoid, T_C = 25 °C	350	Α
T _J	Operating junction temperature range		-55 to 150	°C
T _{STG}	Storage temperature		-55 to 150	°C
	Lead soldering temperature	Leads at 1.6 mm from case, 10 sec	300	°C
V_{ISOL}	Isolation voltage (leads-to-tab)	AC, TO-220	2500	V
P_D	Power dissipation	T _C = 25 °C	78	W

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Thermal Resistance

Symbol	Resistance from:	Conditions	Rating	Units
$R_{\theta JA}$	Junction to ambient	TO-220	62	°C/W
$R_{\theta JC}$	Junction to case		1.6	°C/W

Electrical Specifications at $T_J = 25$ °C (unless otherwise specified)

Symbol	Parameter	Conditions	N	1in	Тур	Max	Units
DC Chara	ecteristics	-					
т	Dovorco current	V _R = 650 V, T _J = 25 °C		-	-	500	μΑ
I_R	Reverse current	$V_R = 650 \text{ V}, T_J = 125 \text{ °}$	V _R = 650 V, T _J = 125 °C		3.7	-	mA
	I _F = 30 A, T _J = 25 °C	I _F = 30 A, T _J = 25 °C		2.75	3.15	V	
V_{F}	Forward voltage	I _F = 30 A, T _J = 150 °C		-	2.32	-	V
C_J	Junction capacitance	V _R = 10 V, 1 MHz	V _R = 10 V, 1 MHz		142	-	pF
Dynamic	Characteristics				-	-	
	Davisina insperiori times	di/dt = 200 A/μs	T _J = 25 °C	-	28.4	-	ns
t _{RR}	Reverse recovery time	$V_R = 400 \text{ V}, I_F = 30 \text{ A}$	T₃ = 125 °C	-	47.0	-	ns
0	Dayana nasayan dabana	di/dt = 200 A/μs	T _J = 25 °C	-	65	105	nC
Q_{RR}	Reverse recovery charge	$V_R = 400 \text{ V}, I_F = 30 \text{ A}$	T₃ = 125 °C	-	180	-	nC
т.	Maximum reverse	di/dt = 200 A/μs	T _J = 25 °C	-	3.5	5.0	Α
I_{RRM}	recovery current		T₃ = 125 °C	-	5.8	-	Α
S Softr	Softness factor = $\frac{t_B}{}$	ul/ut = 200 A/μ3	T _J = 25 °C	-	0.5	-	
	Softness factor = $\frac{B}{t_A}$		T₃ = 125 °C	-	0.3	-	

Note to component engineers: H-Series diodes employ Schottky technologies in their design and construction. Therefore, Component Engineers should plan their test setups to be similar to those for traditional Schottky test setups. (For additional details, see Application Note AN-300.)

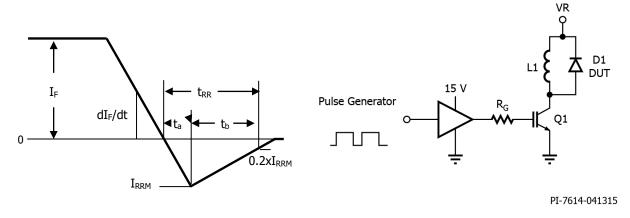


Figure 1. Reverse Recovery Definitions

Figure 2. Reverse Recovery Test Circuit

Electrical Specifications at $T_J = 25$ °C (unless otherwise specified)

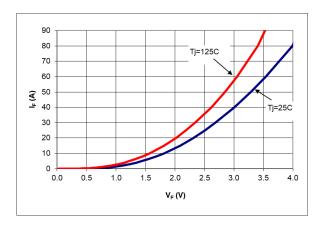


Figure 3. Typical I_F vs V_F

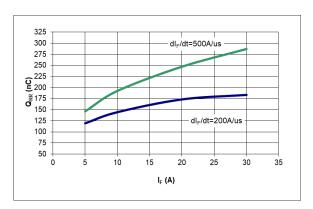


Figure 5. Typical Q_{RR} vs I_F at T_J = 125 °C

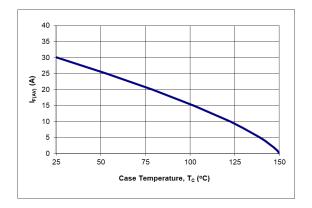


Figure 7. DC Current Derating Curve

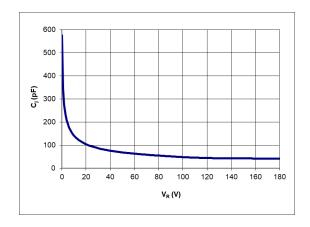


Figure 4. Typical C_j vs V_R

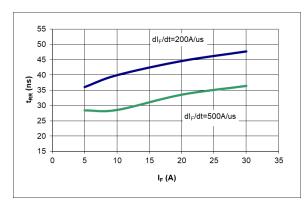


Figure 6. Typical t_{RR} vs I_F at $T_J = 125$ °C

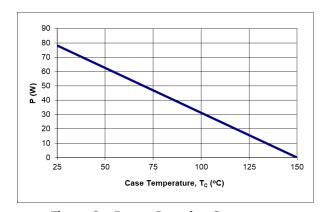


Figure 8. Power Derating Curve

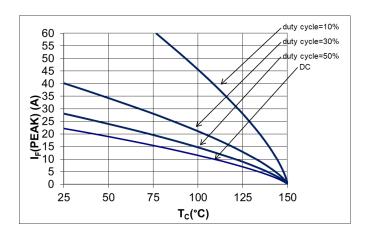


Figure 9. $I_F(PEAK)$ vs T_C , f = 70 kHz

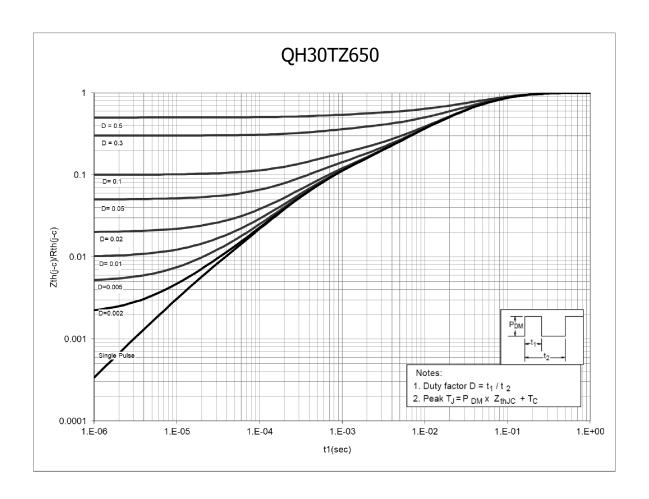
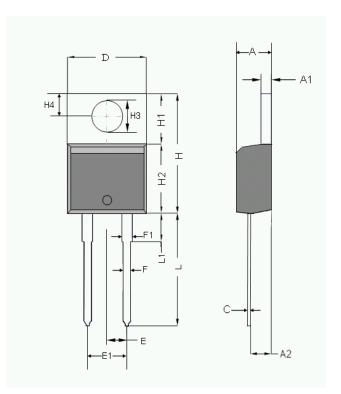


Figure 10. Normalized Maximum Transient Thermal Impedance



Dimensional Outline Drawings

TO-220AC



	Millimeters		
Dim	MIN	MAX	
A	4.32	4.70	
A1	1.14	1.40	
A2	2.03	2.79	
С	0.34	0.610	
D	9.65	10.67	
E	2.49	2.59	
E1	4.98	5.18	
F	0.508	1.016	
F1	1.14	1.78	
Н	14.71	16.51	
H1	5.84	6.795	
H2	8.40	9.00	
Н3	3.53	3.96	
Н4	2.54	3.05	
L	12.70	14.22	
L1	-	6.35	

Mechanical Mounting Method	Maximum Torque / Pressure specification
Screw through hole in package tab	1 Newton Meter (nm) or 8.8 inch-pounds (lb-in)
Clamp against package body	12.3 kilogram-force per square centimeter (kgf/cm²) or 175 lbf/in²

Soldering time and temperature: This product has been designed for use with high-temperature, lead-free solder. The component leads can be subjected to a maximum temperature of 300 °C, for up to 10 seconds. See Application Note AN-303, for more details.

Ordering Information

Part Number	Package	Packing
QH30TZ650	TO-220AC	50 units/tube

The information contained in this document is subject to change without notice.



Revision	Notes	Date
1.0	Production release.	0824



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